

**SHEET INDEX**

CONTENTS	SHEET NO.	ISSUE NO.																			
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
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	F2			3	3	5	5	5	5	5	5	5									

DWG ISS	CD	DWG	CD	DWG	CD
ISSUE	ISSUE	ISSUE	ISSUE	ISSUE	ISSUE
1	1	2D	APP	3D	2D
4D	2D	APP	5B	APP	6B
7B	3B	8D	APP		
DWG	CD	DATE	DRWN	APPD	
ISSUE	ISSUE	ISSUED			
9B	3B	3-27-75	OL	RFNC	110
10B	3B	11-10-77	OL	CDI PD	
11D	3B	5-8-79	OL	JWW	
	4D			JRM	
				JS	

**SHEET INDEX NOTES**

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

**SUPPORTING INFORMATION**

CATEGORY	NO.

**NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.**

ISSUE  
**11D**

1K03  
STATION SYSTEMS  
KEY TELEPHONE SYSTEM NO. 1A2  
TIE LINE AND STATION LINE " CIRCUITS  
BELL TELEPHONE LABORATORIES  
INCORPORATED

AT&TCO STANDARD  
**SD-69559-01-A1**  
20 SHEETS  
DWG SIZE  
**3S**  
PRINTED IN U.S.A.

SD-69559-01-A1

HANDBK DWG

DRAWING  
ISSUE

3D  
6B  
7B

DR  
PC

OPTION INDEX

APP OR WRC	LOCATION
Z	1F3, 4D5, 5E2
Y	2D4
X	1E3, 4D5
W	1Q3, 2Q2, 3F3, 4F3, 5E2
V	1Q3, 2Q3, 3F4, 4F2, 5F2
T	1Q3, 2Q2, 3F4, 4F3, 5E2, 5F2
S	1Q3, 2Q2, 3F3, 4F3, 5E2
R	1Q3, 4G3, 5E2
Q	1Q3, 4F3, 5D2, 5E2
N	1Q3, 4G3, 5F2
M	1A5, 2B1, 3D6, 4C7, 5D2
K	5E2
H	5D2
G	APP FIG. 1, 4, 5, 1A2, 1B2, 4A4, 4C4, 5A4, 5B4
F	APP FIG. 1, 4, 5, 1A2, 1B2, 4B4, 4C4, 5A4, 5B4
E	APP FIG. 1, 4, 1B4, 4B6
D	APP FIG. 4, 4C5

ISSUE  
10B

TIE LINE AND STATION LINE CIRCUITS

SD-69559-01-A2

BELL TELEPHONE LABORATORIES  
INCORPORATED

3S

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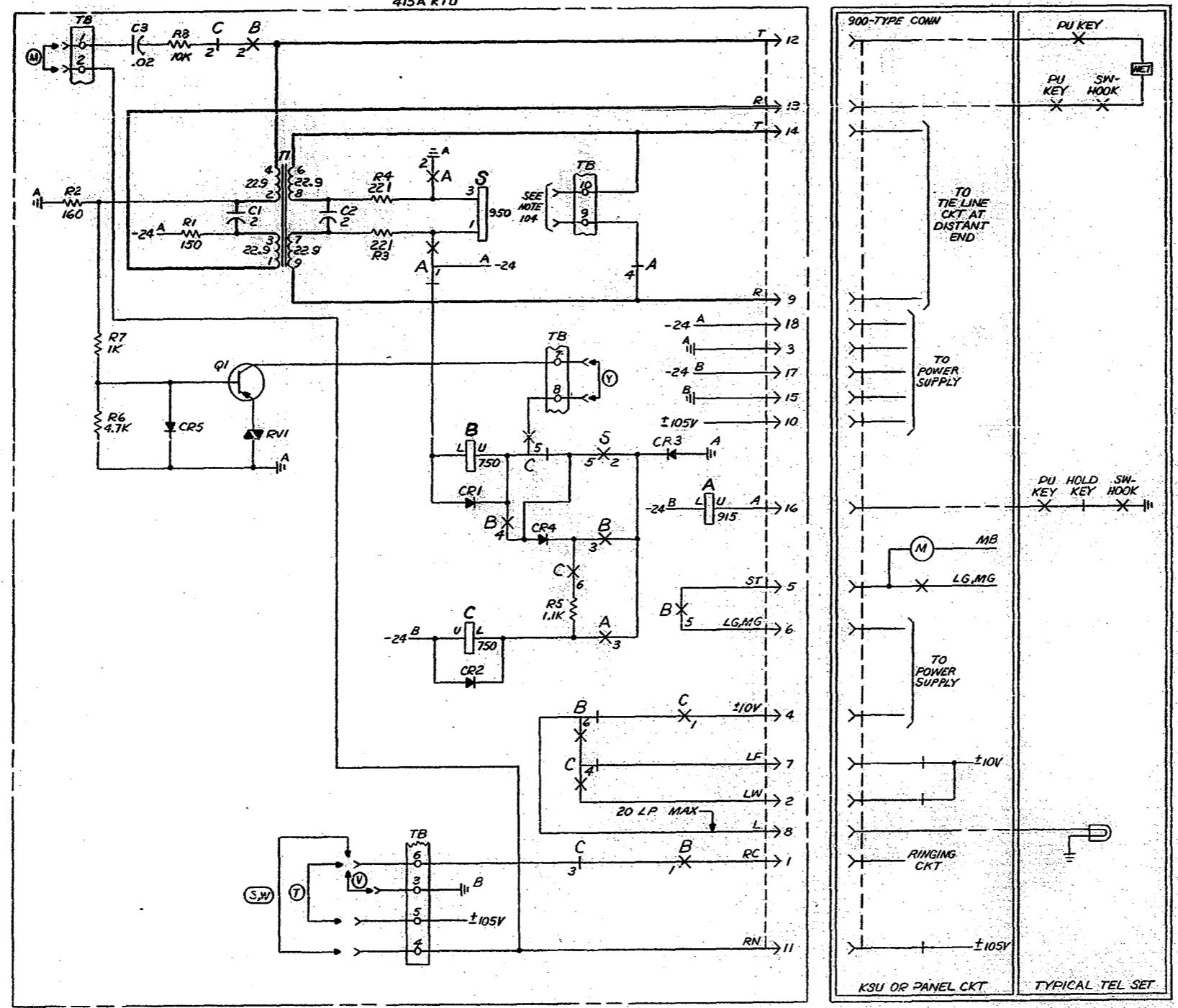
SD-69559-01-A2

8-7484-9 (8-68)

# FS 2

TIE LINE CKT - AUTOMATIC DC SIGNALING

415A KTU

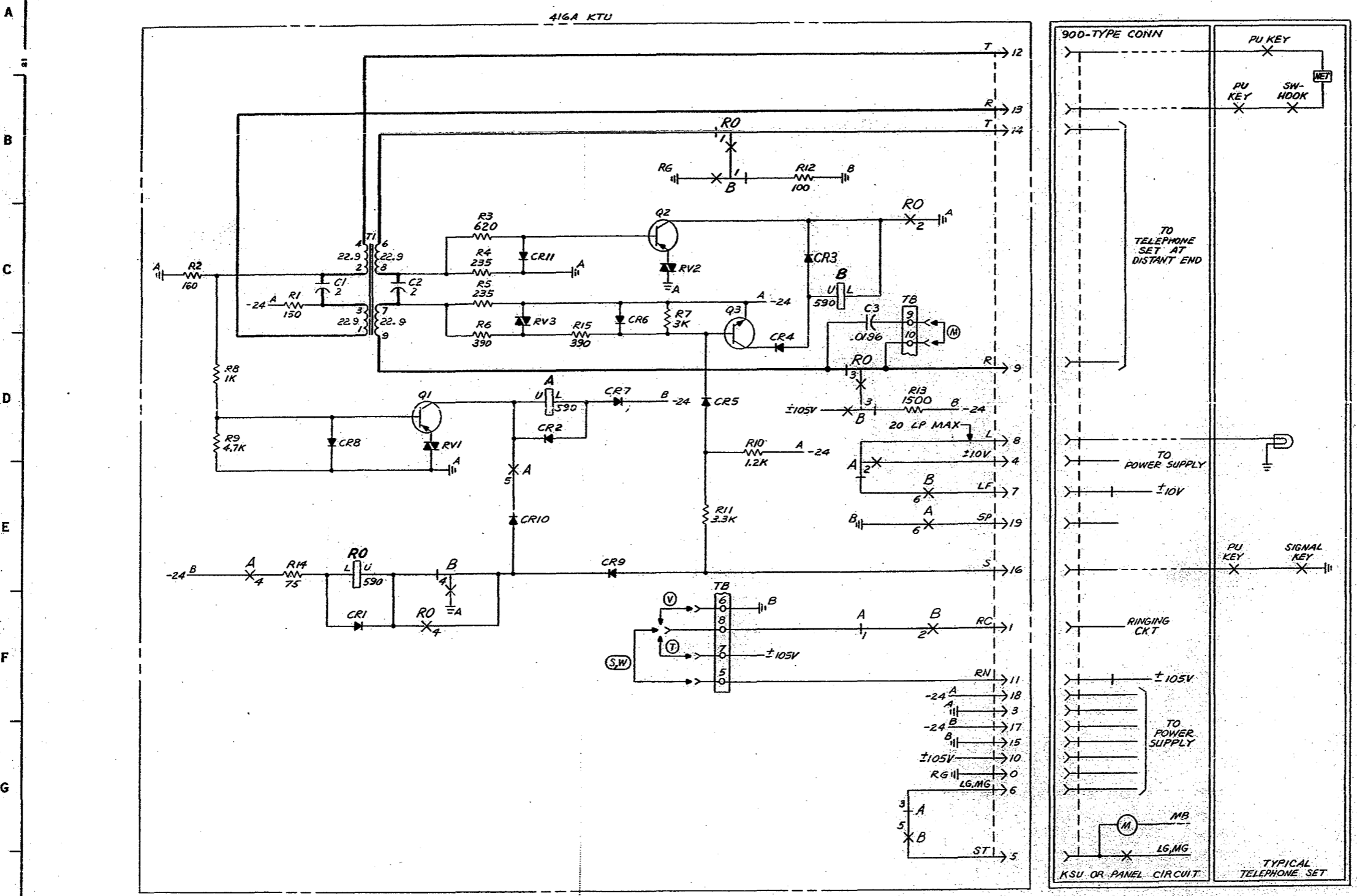


DRAWING ISSUE	
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20	CHG
30	REV
5B	REV

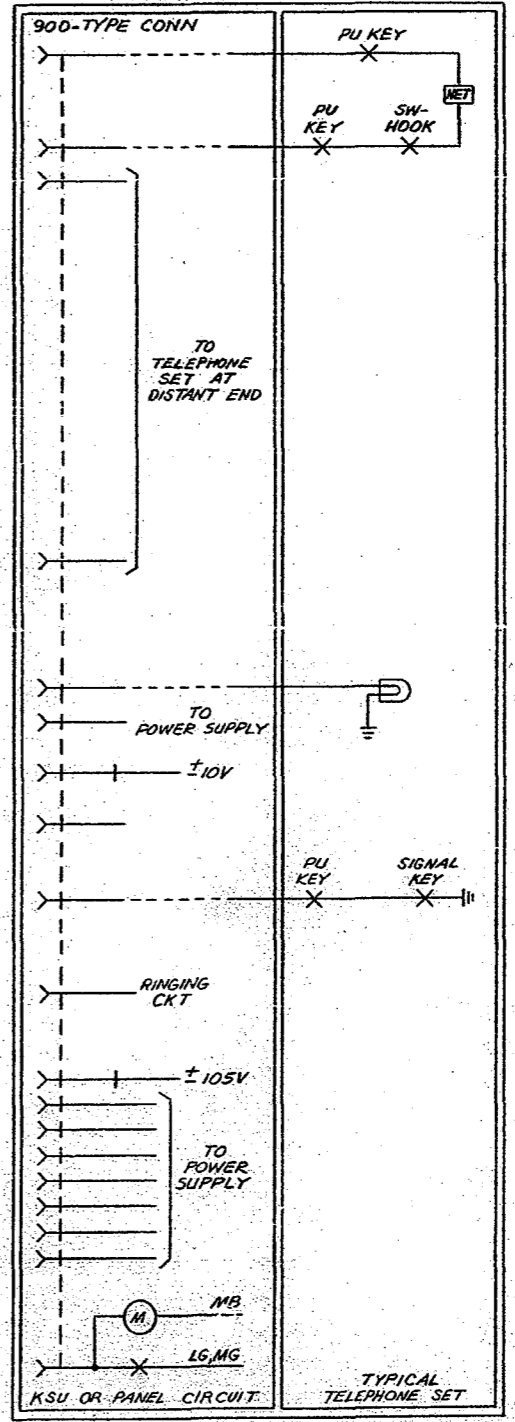
SD-69559-01-B2

5

FS 3  
STATION LINE CIRCUIT



DRAWING ISSUE	
1	REV
20	REV
30	REV
40	REV
5B	REV



SD-69559-01-B3

TIE LINE AND STATION LINE CIRCUITS

BELL TELEPHONE LABORATORIES  
INCORPORATED

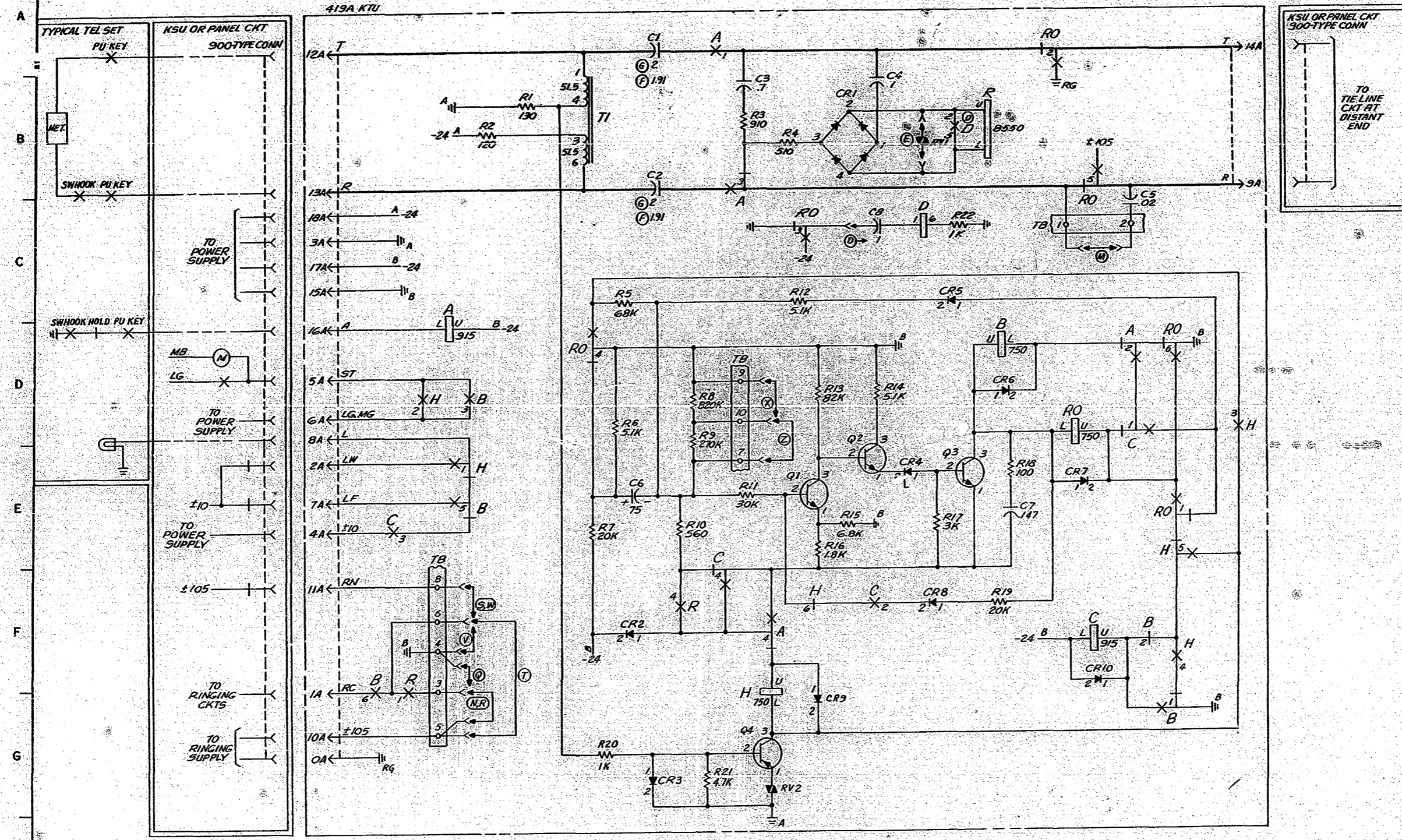
SD-69559-01-B3

65

5

**FS 4**  
TIE LINE CKT-AUTOMATIC RINGDOWN SIGNALING

DRAWING ISSUE  
3D  
4D  
5B  
6B  
7B



SD-69559-01-B4

ISSUE  
10B

TIE LINE AND STATION LINE CIRCUITS

SD-69559-01-B4

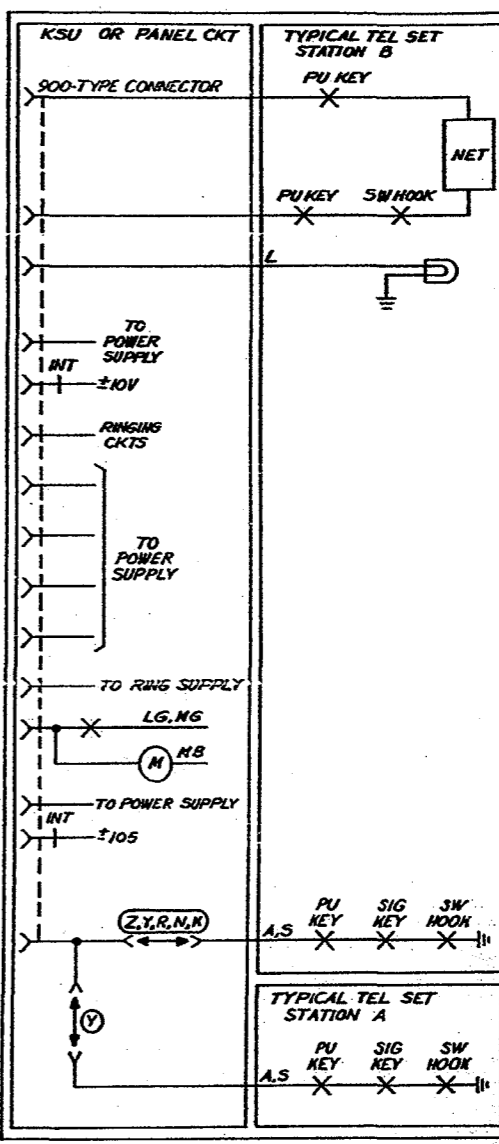
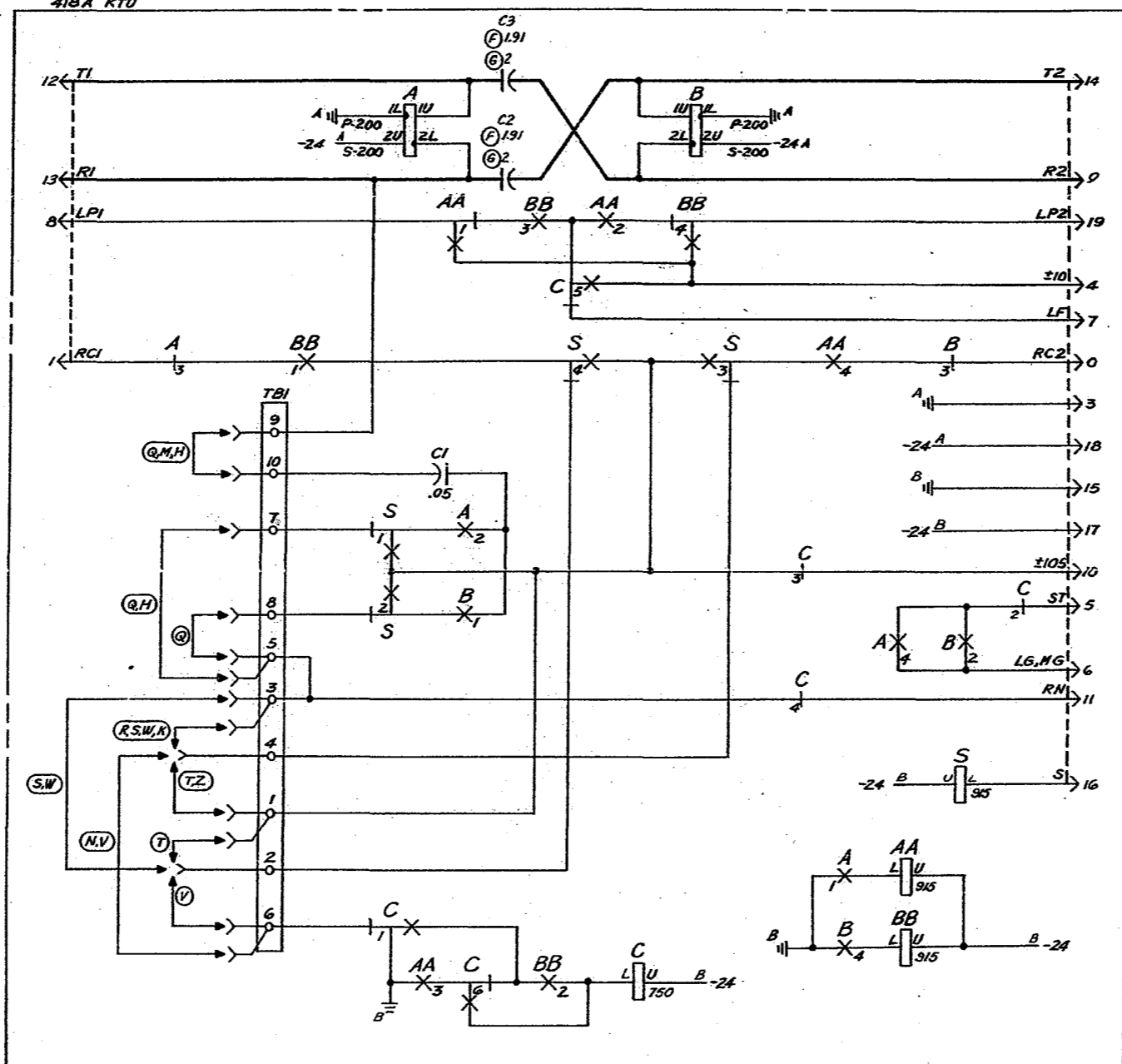
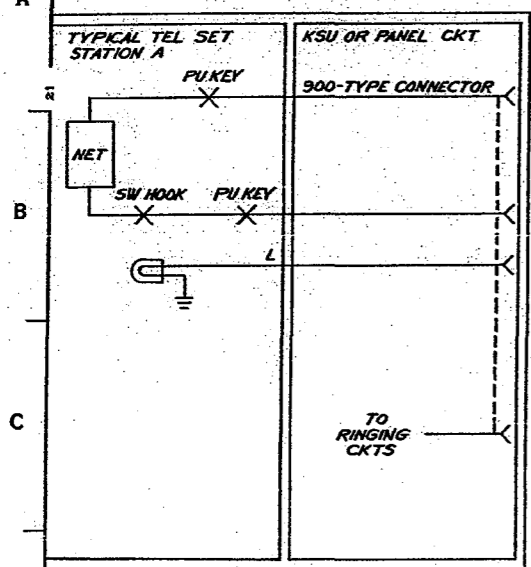
BELL TELEPHONE LABORATORIES  
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65 PRINTED IN U.S.A.

# FS 5

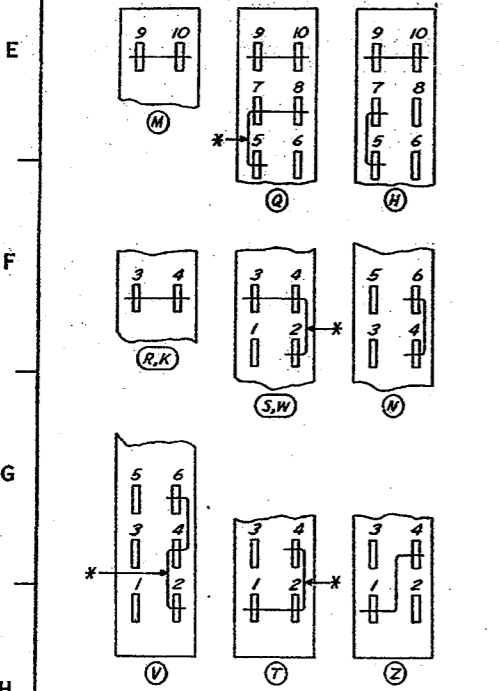
TIE LINE CKT- SHORT RANGE  
SEE FS 101

DRAWING	ISSUE
30	DLD
5B	DLD
6B	D&A



## FS 101

SUGGESTED METHOD FOR STRAPPING WIRING  
OPTIONS ON TBI OF 418A KTU



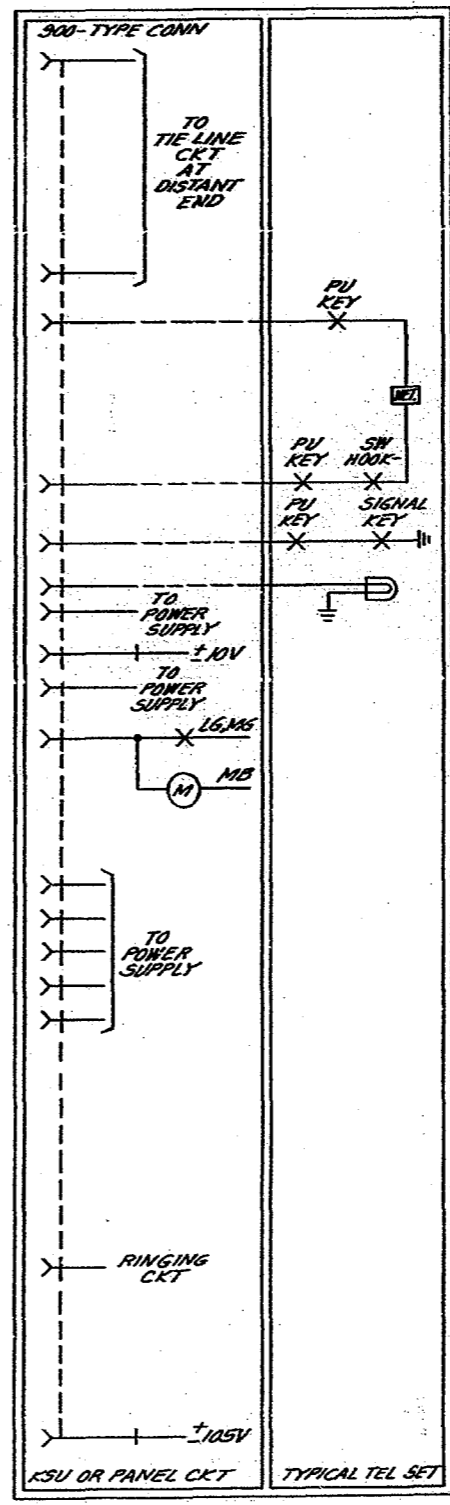
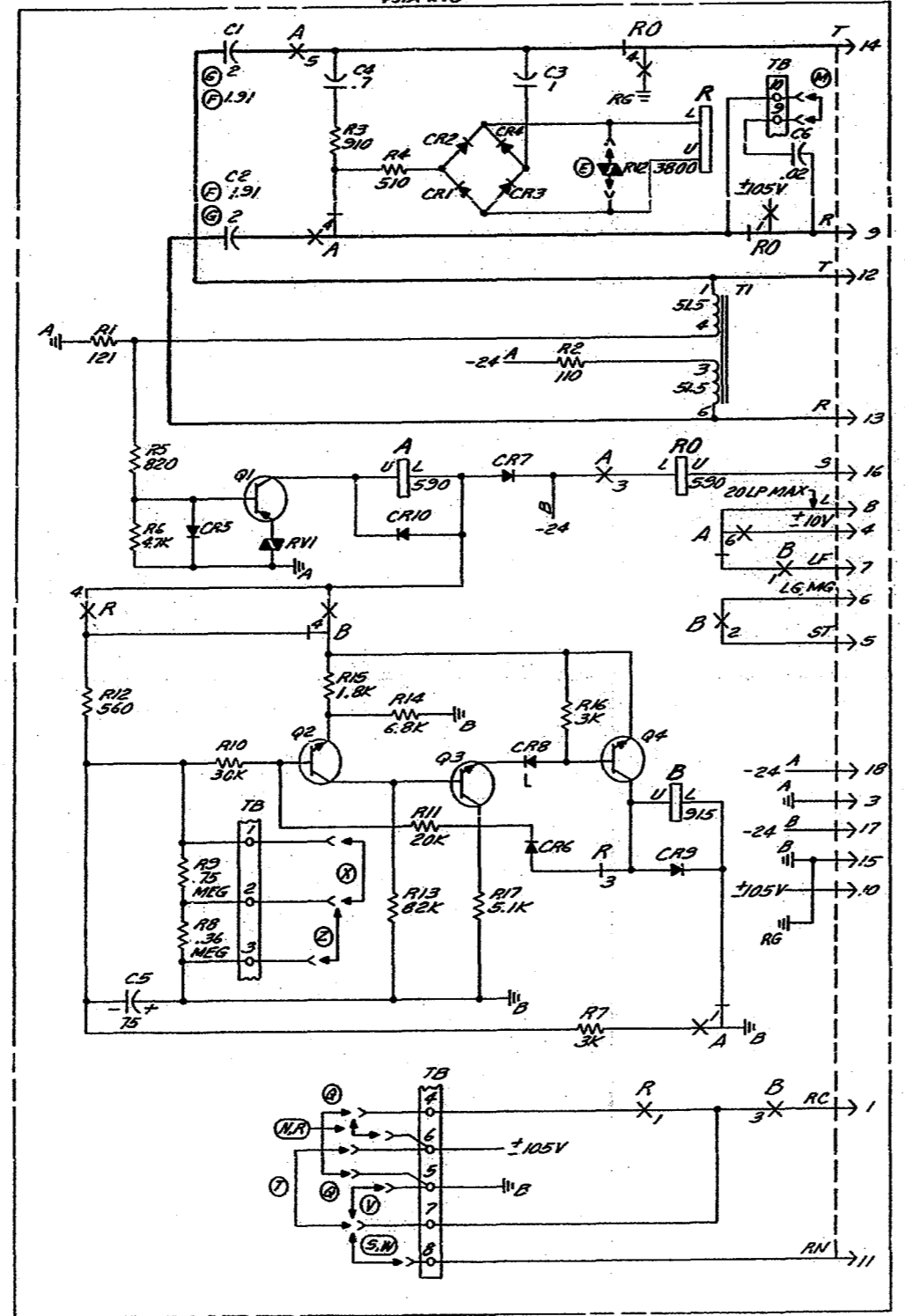
SD-69559-01-B5

0 1 2 3 4 5 6 7 8 9

### FS 6

TIE LINE CKT-RINGDOWN MANUAL SIGNALING

461A KTU



ISSUE 9B

SD-69559-01-B6

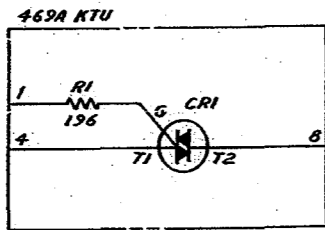
0 1 2 3 4 5 6 7 8 9

0 1 2 3 4 5 6 7 8 9

A  
B  
C  
D  
E  
F  
G  
H

A  
B  
C  
D  
E  
F  
G  
H

**FS 7**  
LAMP EXTENDER



SD-69559-01-87

MADE IN U.S.A. 65-40717

0 1 2 3 4 5 6 7 8 9

TIE LINE AND STATION LINE CIRCUITS		SD-69559-01-87
BELL TELEPHONE LABORATORIES INCORPORATED	6S PRINTED IN U.S.A.	

ISSUE  
9B



# APP FIG. 1

414A KTU

## RELAY

DESIG	A		B		R		RO	
CODE	MB1		MA19		MA29		MA5	
OPTION	CONT. ARR.	LOC	CONT. ARR.	LOC	CONT. ARR.	LOC	CONT. ARR.	LOC
6	EBM	1D5						
5	EBM	1A3						
4	EBM	1B3	EMB	1D3	M	1D2	EBM	1A4
3	EUM	1C4	M	1D5	B	1E4	EBM	
2	EBM	1D5	M	1D5	B		EBM	
1	EBM	1F5	EBM	1D5	M	1G4	EBM	1B5
COIL	1C3	1E5	1A5	1C5				

## TERMINAL BOARD

DESIG	TB
CODE	
OPTION	
10	1A5
9	1A5
8	1G3
7	1G3
6	1G3
5	1G3
4	1G3
3	1F3
2	1F3
1	1E3

## CAPACITOR

DESIG	LOC	CODE
C1	1A2	535GD, 1.91
C2	1B2	535GD, 1.91
C3	1A4	542N, 1
C4	1A3	535JF, .17
C5	1F2	KS-16390 L12, 75
C6	1B5	542AA, .02

## TRANSISTOR

DESIG	LOC	CODE
Q1	1C2	12H
Q2	1E3	16G
Q3	1E4	16G
Q4	1E4	16G

## DIODE

DESIG	LOC	CODE
CR1	1B3	
CR2	1A3	456B
CR3	1B4	
CR4	1A4	
CR5	1D2	
CR6	1E4	458A
CR7	1C4	
CR8	1E4	459E
CR9	1E5	
CR10	1D3	400J

## VARIATOR

DESIG	LOC	CODE
RV1	1D3	100G
RV2	1B4	317A

## INDUCTOR

DESIG	LOC	CODE
T1	1B5	1682A

## RESISTOR

DESIG	LOC	CODE
R1	1C2	KS-14603 L1A, 121
R2	1C4	KS-14603 L1A, 110
R3	1B3	KS-13490 L1, 910
R4	1B3	KS-13490 L2, 510
R5	1C2	KS-13490 L1, 820
R6	1D2	KS-13490 L1, 4.7K
R7	1F4	KS-13490 L2, 3K
R8	1F2	KS-13490 L1, .36 MEG
R9	1E2	KS-13490 L2, .75 MEG
R10	1E2	KS-13490 L1, 30K
R11	1E3	KS-13490 L2, 20K
R12	1E2	KS-13490 L1, 560
R13	1F3	KS-13490 L1, 82K
R14	1E3	KS-13490 L1, 6.8K
R15	1D3	KS-13490 L1, 1.8K
R16	1E4	KS-13490 L1, 3K
R17	1F4	KS-13490 L1, 5.1K

## DRAWING ISSUE

1	SUM	DATE	BY
3D	MLL	DATE	BY
4D	MLL	DATE	BY
5B	MLL	DATE	BY
6B	MLL	DATE	BY
7B	MLL	DATE	BY

SD-69559-01-C1

TIE LINE AND STATION LINE CIRCUITS

SD-69559-01-C1

BELL TELEPHONE LABORATORIES  
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# APP FIG. 2

415A KTU

DRAWING ISSUE

1	ENR	DND	4.0
3D	ENR	DLD	4.0
	ENR	DLV	4.0

### RELAY

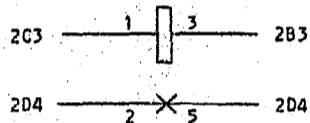
DESIG	A		B		C	
CODE	MA19		MB16		MB17	
OPTION	CONT	LOC	CONT	LOC	CONT	LOC
6			EBM	2E4	EBM	2E4
5			M	2E4	EBM	2D4
4	FMB	2C4	M	2E3	EBM	2E4
3	M	2E4	M	2E4	EBM	2D4
2	M	2B3	M	2A2	EBM	2A2
1	EBM	2C3	EBM	2G4	EBM	2E4
COIL		2D5		2D3		2E3

### TERMINAL BOARD

DESIG	TB
CODE	
OPTION	
10	2B4
9	2C4
8	2D3
7	2C3
6	2G3
5	2G3
4	2G3
3	2G3
2	2B1
1	2A1

### RELAY

S  
327B



### TRANSFORMER

DESIG	LOC	CODE
T1	2B2	2578U

### TRANSISTOR

DESIG	LOC	CODE
Q1	2D2	12H

### VARISTOR

DESIG	LOC	CODE
RV1	2D2	100G

### CAPACITOR

DESIG	LOC	CODE
C1	2C2	542F, 2
C2	2C2	
C3	2A1	542AA, .02

### DIODE

DESIG	LOC	CODE
CR1	2D3	400J
CR2	2F3	
CR3	2D4	
CR4	2E4	458A
CR5	2D2	

### RESISTOR

DESIG	LOC	CODE
R1	2C2	KS-14603 L1C, 150
R2	2B1	KS-14603 L1C, 160
R3	2C3	KS-14603 L3C, 221
R4	2B3	KS-14603 L3C, 221
R5	2E4	KS-13490 L1, 1.1K
R6	2D1	KS-13490 L1, 4.7K
R7	2C1	KS-13490 L1, 1K
R8	2A2	KS-13490 L3, 10K

SD-69559-01-C2

TIE LINE AND STATION LINE CIRCUITS

SD-69559-01-C2

BELL TELEPHONE LABORATORIES  
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2  
3S

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3

5

APP FIG. 3  
416A KTU

RELAY						
DESIG	A		B		RD	
CODE	MB5		MB1		MAS	
OPTION	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC
6	EMB	3E6	EMB	3E6		
5	EMB	3E3	EMB	3E6		
4	EMB	3E1	EMB	3E2	EMB	3F2
3	EMB	3G6	EMB	3D5	EMB	3D5
2	EMB	3E5	EMB	3F6	EMB	3C5
1	EMB	3F5	EMB	3B4	EMB	3B4
COIL		3D3		3C5		3E2

TRANSFORMER		
DESIG	LOC	CODE
T1	3C2	2578U

TRANSISTOR		
DESIG	LOC	CODE
Q1	3D2	12H
Q2	3C4	12H
Q3	3C4	16G

CAPACITOR		
DESIG	LOC	CODE
C1	3C1	542F, 2
C2	3C2	
C3	3C5	594G, .0196

VARISTOR		
DESIG	LOC	CODE
RV1	3D2	100G
RV2	3C4	100G
RV3	3C3	317D

DIODE		
DESIG	LOC	CODE
CR1	3F2	400J
CR2	3D3	
CR3	3C5	
CR4	3D5	
CR5	3D4	458A
CR6	3C4	
CR7	3D3	
CR8	3D2	
CR9	3E3	458A
CR10	3E3	
CR11	3C3	

RESISTOR		
DESIG	LOC	CODE
R1	3C1	KS-14603 L1C, 150
R2	3C0	KS-14603 L1C, 160
R3	3C3	KS-13490 L1, 620
R4	3C3	KS-14603 L3C, 235
R5	3C3	KS-14603 L3C, 235
R6	3D3	KS-13490 L1, 390
R7	3C4	KS-13490 L1, 3K
R8	3D1	KS-13490 L1, 1K
R9	3D1	KS-13490 L1, 4.7K
R10	3D4	KS-13490 L1, 1.2K
R11	3E4	KS-13490 L1, 3.3K
R12	3B5	KS-13490 L3, 100
R13	3D6	KS-13490 L3, 1500
R14	3E1	KS-13490 L1, 75
R15	3D3	KS-13490 L1, 390

TERMINAL BOARD	
DESIG	TB
CODE	
OPTION	
10	3C5
9	3C5
8	3F4
7	3F4
6	3F4
5	3F4
4	
3	
2	
1	

DRAWING ISSUE	
1	SUM DYC DLY
3D	MAL DLD DLY
4D	
5B	

SD-69559-01-C3

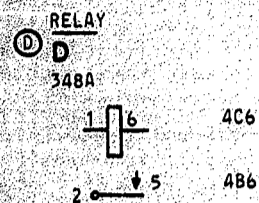
TIE LINE AND STATION LINE CIRCUITS		SD-69559-01-C3
BELL TELEPHONE LABORATORIES INCORPORATED		
DWR RISE 3S		PRINTED IN U.S.A.

13207  
DUNLAP PRESS, INC. APPROOF 15/11/53

# APP FIG. 4

419A-KTU

RELAY														
DESIG	A		B		C		H		R		RO			
CODE	MA4		MB17		MA4		MB17		MA29		MB17			
OPTION	CONT	LOC	CONT	LOC	CONT	LOC	CONT	LOC	CONT	LOC	CONT	LOC		
	ARR		ARR		ARR		ARR		ARR		ARR			
6			EMB	4F2					EMB	4F5			EMB	4D8
5			EMB	4E3					EMB	4E8			EMB	4C7
4	EMB	4F5	EMB		EMB	4E4			EMB	4F8	M	4F4	EMB	4D3
3	EMB	4C4	EMB	4D3	EMB	4E2			EMB	4D8	B		EMB	4C5
2	EMB	4D7	EMB	4F7	EMB	4F5			EMB	4D2			EMB	4A7
1	EMB	4A4	EMB	4G8	EMB	4D7			EMB	4E3	M	4F2	EMB	4F6
COIL		4C2		4D6		4F7				4F5			4B6	4D7



### CAPACITOR

DESIG	LOC	CODE
C1	4A4	535GD, 1.91
C2	4B4	535GD, 1.91
C3	4B5	535JF, .7
C4	4B6	542N, 1
C5	4C7	542AA, .02
C6	4E4	KS-16390 L12, 75
C7	4E7	594G, .147
C8	4C5	KS-20736 L1, 1

### TERMINAL BOARD

DESIG	TB
10	4D5
9	4D5
8	4F2
7	4D5
6	4F2
5	4G2
4	4F2
3	4F2
2	4C7
1	4C7

### VARIATOR

DESIG	LOC	CODE
RV1	4B6	317A
RV2	4G5	100G

### DIODE

DESIG	LOC	CODE
CR1	4B5	460A
CR2	4F4	458A
CR3	4G4	458A
CR4	4E6	459E
CR5	4C6	458A
CR6	4D6	441J
CR7	4E7	441J
CR8	4F6	458A
CR9	4G5	441J
CR10	4F7	441J

### INDUCTOR

DESIG	LOC	CODE
T1	4B3	1682A

### RESISTOR

DESIG	LOC	CODE
R1	4B3	KS-14803 L1A, 130
R2	4B3	KS-14803 L1A, 120
R3	4B5	KS-13490 L1, 910
R4	4B5	L2, 810
R5	4C4	L1, 68K
R6	4D4	L1, 5.1K
R7	4E3	L1, 20K
R8	4D4	L1, 820K
R9	4D4	L1, 270K
R10	4E4	L1, 560
R11	4E4	L1, 30K
R12	4C5	L1, 5.1K
R13	4D5	L1, 82K
R14	4D6	L1, 5.1K
R15	4E5	L1, 6.8K
R16	4E5	L1, 1.8K
R17	4E6	L1, 3K
R18	4E7	L1, 100
R19	4F6	L2, 20K
R20	4G3	L1, 1K
R21	4G4	KS-13490 L1, 4.7K
R22	4C6	KS-13490 L1, 1K

### TRANSISTOR

DESIG	LOC	CODE
Q1	4E5	16G
Q2	4E5	16G
Q3	4E6	16G
Q4	4G5	12H

DRAWING ISSUE

3D  
5B  
6B  
7B

ISSUE 10B

SD-69559-01-C4

TIE LINE AND STATION LINE CIRCUITS

2

SD-69559-01-C4

BELL TELEPHONE LABORATORIES

35

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# APP FIG. 5

410A KTU

RELAY

DESIG CODE	A MA 24		AA MA 1		B MA 24		BB MA 1				C MB 17				S MA 1		DESIG CODE
	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	
6											EMB	SF4					6
5											EMB	SC4					5
4	M	SE6	EBM	SC6	M	SF6	EBM	SB5			EMB	SE6		EBM	SC4		4
3	B	SC2	EBM	SF4	B	SC6	EBM	SB4			EMB	SD5		EBM	SC5		3
2	M	SD4	EBM	SB4	M	SE6	EBM	SF4			EMB	SD7		EBM	SE3		2
1	M	SF6	EBM	SB4	M	SD4	EBM	SC3			EMB	SF3		EBM	SD3		1
COIL		SB3		SF6		SB5		SF6				SF5			SE6		COIL

CAPACITOR

DESIG	LOC	CODE
C1	SD4	542W, .05
C2	SB4	538G0, 1.91
C3	SA4	538G0, 1.91

TERMINAL BOARD

DESIG	TB1
CODE	77A
OPTION	
10	SD3
9	SC3
8	SD3
7	SD3
6	SF3
5	SE3
4	SC3
3	SE3
2	SF3
1	SF3

DRAWING  
ISSUE

3D  
ML  
DLB  
DLV

6B DR

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6

TIE LINE AND STATION LINE CIRCUITS

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APP FIG. 6

461A KTU

RELAY

DESIG	A		B		R		PO	
CODE	MB1		MA19		MA29		MA5	
OPTION	CONT	LOC	CONT	LOC	CONT	LOC	CONT	LOC
	ARR		ARR		ARR		ARR	
6	EBM	1D5						
5	EBM	1A3						
4	EBM	1B3	EMB	1D3	M	1D2	EBM	1A4
3	EBM	1C4	M	1G5	B	1E4	EBM	
2	EBM		M	1D5	B		EBM	
1	EBM	1F5	EBM	1D5	M	1G4	EBM	1B5
COIL	X	1C3	X	1E5	X	1A5	X	1C5

TERMINAL BOARD

DESIG	TB
10	1A5
9	1A5
8	1G3
7	1G3
6	1G3
5	1G3
4	1G3
3	1F3
2	1F3
1	1E3

CAPACITOR

DESIG	LOC	CODE
C1	1A2	535GD, 1.91
C2	1B2	535GD, 1.91
C3	1A4	542N, 1
C4	1A3	535JF, .7
C5	1F2	KS-16390 L12, 75
C6	1B5	542AA, .02

TRANSISTOR

DESIG	LOC	CODE
Q1	1C2	12H
Q2	1E3	16G
Q3	1E4	16G
Q4	1E4	16G

DIODE

DESIG	LOC	CODE
CR1	1B3	
CR2	1A3	456B
CR3	1B4	
CR4	1A4	
CR5	1D2	
CR6	1E4	458A
CR7	1C4	
CR8	1E4	459E
CR9	1E5	
CR10	1D3	400J

VARISTOR

DESIG	LOC	CODE
RV1	1D3	100G
RV2	1B4	317A

INDUCTOR

DESIG	LOC	CODE
T1	1B5	1682A

RESISTOR

DESIG	LOC	CODE
R1	1C2	KS-14603 L1A, 121
R2	1C4	KS-14603 L1A, 110
R3	1B3	KS-13490 L1, 910
R4	1B3	KS-13490 L2, 510
R5	1C2	KS-13490 L1, 820
R6	1D2	KS-13490 L1, 4.7K
R7	1F4	KS-13490 L2, 3K
R8	1F2	KS-13490 L1, .36 MEG
R9	1E2	KS-13490 L2, .75 MEG
R10	1E2	KS-13490 L1, 30K
R11	1E3	KS-13490 L2, 20K
R12	1E2	KS-13490 L1, 560
R13	1F3	KS-13490 L1, 82K
R14	1E3	KS-13490 L1, 6.8K
R15	1D3	KS-13490 L1, 1.8K
R16	1E4	KS-13490 L1, 3K
R17	1F4	KS-13490 L1, 5.1K

DRAWING  
ISSUE

ISSUE  
8D

TIE LINE AND STATION LINE CIRCUITS

1

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**APP FIG. 7**

469A KTU

RESISTOR		
DESIG	LOC	CODE
R1	7B0	KS-14603 L3A, 196

THYRISTOR		
DESIG	LOC	CODE
CR1	7B1	40575 RCA, 15AMP

DRAWING ISSUE

A

B

C

D

E

F

G

H

9B

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TIE LINE AND STATION LINE CIRCUITS

1

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CIRCUIT NOTES:

DESIG	FUSE AMP	POTENTIAL	ONE PER
A		-24 TALK	X
B		-24 SIG	X
<b>BATTERY SYMBOL</b>		<b>VOLTAGE RANGE</b>	
-24		20-26	

X FUSE PROVIDED ON ASSOCIATED KSU OR PANEL CKT.

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WRG	QUANTITY
TIME-OUT CONTROL	1,4	X	1 PER LINE
		Z	
		X	
VISUAL HOLD	2	Y	
AUDIBLE SIGNAL	2,3	W	1 PER LINE
		T	
		V	
		S	
	1,4	W	1 PER LINE
		T	
		V	
		S	
		R	
		Q	
AUDIBLE RINGBACK SEE NOTE 105	1,2,3,4	M	
SIGNALING	5	W	1 PER LINE
		T	
		V	
		S	
	1	R	1 PER LINE
		Z	
		N	
		K	
		Y	
		M	
AUDIBLE RINGBACK SEE NOTE 105		Q	1 PER LINE
		H	
		M	

X LONG-TIME DELAY IS FUNCTION OF CIRCUIT WITH OPTION X AND Z REMOVED.

RECORD OF APP FIGURES, WIRING, AND APPARATUS CHANGES						
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	A&M	MD
6B	F OR G	G		F		G
7B	E	E				E
10B	D	NONE		D		

104. CONNECT 910-OHM RESISTOR AND 2-MICROFARAD CAPACITOR IN SERIES BETWEEN TERMINALS 9 AND 10 FOR AN IDLE LINE TERMINATION.  
RES. KS-13490, L1, 910  
CAP. 542F, 2 UF
105. THE AUDIBLE RINGBACK SIGNAL IS SATISFACTORY ONLY IF THE RINGING SUPPLY CONTAINS AN AUDIBLE COMPONENT.
106. WHEN MORE THAN ONE POWER SUPPLY IS USED TO SUPPLY TALK BATTERY AND SIGNAL BATTERY, THE GROUND TERMINALS OF THE TWO SUPPLIES ARE TO BE BONDED TOGETHER.
107. THE CIRCUIT LOOP BETWEEN THE STATION AND THE KTU SHALL NOT EXCEED 50 OHMS.
108. FOR MANUAL SIGNALING, THE AUDIBLE SIGNAL AT THE DISTANT STATION IS UNDER THE CONTROL OF THE (S) RELAY AND MAY BE CONNECTED WITH OR WITHOUT COMMON AUDIBLE. COMMON AUDIBLE MUST BE FURNISHED BY MEANS OF A DIODE MATRIX.
109. WHERE LAMPS ARE OPERATED FROM THE 418A KTU WITHOUT AUXILIARY EQUIPMENT, THE NORMAL 50 OHM LOOP RANGE APPLIES.
110. STATION "A" IS ALWAYS ASSIGNED AS THE AUTOMATIC SIGNALING STATION WHENEVER THE ONE-WAY AUTOMATIC, ONE-WAY MANUAL SIGNALING OPTION IS USED.

DRAWING ISSUE

3D  
ML  
DLA  
DLV  
5B  
6B  
7B

ISSUE  
10B

TIE LINE AND STATION LINE CIRCUITS

2

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INFORMATION NOTES:

- 301. UNLESS OTHERWISE SPECIFIED:  
RESISTANCE VALUES ARE IN OHMS,  
CAPACITANCE VALUES ARE IN MICROFARADS,  
VALUES PRECEDED BY THE SYMBOL + (PLUS)  
OR - (MINUS) ARE IN VOLTS.
- 302. THESE RINGING RANGES ARE ALSO VALID WITH UNIGAUGE  
PLANT WHICH MAY UTILIZE AN E6 REPEATER IN EACH  
SUBSCRIBER LOOP.

WORKING LIMITS:

MAXIMUM RINGING RANGES  
IN OHMS FOR 414A AND 419A

FREQUENCY	MINIMUM GENERATOR RINGING VOLTAGE (RMS)	MINIMUM LEAKAGE RESISTANCE = 20K
		RINGING RANGE (OHMS) SEE NOTE 302
20-CYCLE	75	4000
	84	4600
	92	5000
30-CYCLE	110	5000
	120	5400

MAXIMUM DC SIGNALING RANGE IN OHMS  
FOR 415A AND 416A WITH A BATTERY  
VOLTAGE OF -20 VOLTS.

KTU	MINIMUM LEAKAGE RESISTANCE	
	10K	15K
	SIGNALING RANGE (OHMS)	
415A	2200	2325
416A	700	750

MAXIMUM DC SIGNALING RANGE  
FOR 418A WITH A BATTERY VOLTAGE OF  
-20 VOLTS IS 100 OHMS. (SEE NOTE 109)

DRAWING ISSUE	
3D	ML DLD DLY
5B	LR

5

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**CIRCUIT REQUIREMENTS**

TIE LINE AND STATION LINE CIRCUITS

DRAWING  
ISSUE

APPARATUS				MECH REQ			CIRCUIT PREPARATION				DIRECT CURRENT FLOW REQ						REMARKS
DESIG	CODE	OPT	FIG.	BSP FIG.	CONT PRESS.	ARM. TRVL	BLOCK OR INSULATE	TEST CLIP DATA		TEST SET PREP	SEE TEST NOTE	TEST WDG	TEST FOR	AFTER SOAK			
								CONN. BAT.	CONN. GRD.					MA.	MA.	MA.	
RELAYS																	
414A KTU																	
A	MB1		1	200				U	L	B/G	2,3		0		25.5		
B	MA19		1	105				U	L	B/G	2,3		0		17		
								U	L	B/G	2,3		R		1.1		
R	MA29		1	6				U	L	B/G	2,3		0		4		
RO	MA5		1	100				U	L	B/G	2,3		0		21		
415A KTU																	
A	MA19		2	105					U	GRD	1,2		0		17		
								U	U	GRD	1,2		R		1.1		
B	MB16		2	201				U	L	B/G	2,3		0		20		
								U	L	B/G	2,3		R		1.2		
C	MB17		2	203				U	L	B/G	2,3		0		21		
								U	L	B/G	2,3		H		7.5		
S	3278		2					1	3	B/G	2,3		0		3.8		
								1	3	B/G	2,3		R		0.4		
416A KTU																	
A	MB5		3					U	L	B/G	2,3		0		25.5		
B	MB1		3					U	L	B/G	2,3		0		25.5		
RO	MA5		3					U	L	B/G	2,3		0		21		
418A KTU																	
A	MA24		5	5				2L	1U	B/G	2,5	P1/P2	0		21		
AA	MA17		5	100				U	L	B/G	2,3		0		17		
								U	L	B/G	2,3		H		7.5		
B	MA24		5	5				2L	1U	B/G	2,5	P1/P2	0		21		
								U	L	B/G	2,3		R		1.1		
BB	MA17		5	100				U	L	B/G	2,3		0		17		
								U	L	B/G	2,3		H		7.5		
C	MB17		5	203				U	L	B/G	2,3		0		21		
								U	L	B/G	2,3		H		7.5		
													NO		11.8		
TC	MA17		5					U	L	B/G	2,3		0		17		
S	MA1		5	100					L	GRD	2,4		0		17		
461A KTU																	
A	MB1		6	200				U	L	B/G	2,3		0		25.5		
B	MA19		6	105				U	L	B/G	2,3		0		17		
								U	L	B/G	2,3		R		1.1		
R	MA29		6	6				U	L	B/G	2,3		0		4		
RO	MA5		6	100				U	L	B/G	2,3		0		2		

30  
5B

1.0  
1.2  
1.4

1.0  
1.2

PAGE 1

ISSUE  
80

TEST NOTES:

1. OPEN A LEAD TO TELEPHONE SETS.
2. RELAY NOT ADJUSTABLE, REPLACE WHERE THERE IS A MALFUNCTION.
3. THE CURRENT FLOW INFORMATION LISTED APPLIES ONLY WHEN THE RELAY TO BE TESTED IS REMOVED FROM THE PRINTED WIRING BOARD.
4. OPEN S LEAD TO TELEPHONE SETS.
5. OPEN TIP AND RING LEADS TO TELEPHONE SET.

TIE LINE AND STATION LINE CIRCUITS

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**CIRCUIT REQUIREMENTS**

TIE LINE AND STATION LINE CIRCUITS

DRAWING ISSUE

3D  
ML  
DLV  
5B

APPARATUS				MECH REQ			CIRCUIT PREPARATION				DIRECT CURRENT FLOW REQ					REMARKS	
DESIG	CODE	OPT	FIG.	BSP FIG.	CONT PRESS.	ARM. TRVL	BLOCK OR INSULATE	TEST CLIP DATA		TEST SET PREP	SEE TEST NOTE	TEST WDG	TEST FOR	AFTER SOAK			
								CONN BAT.	CONN GRD					MA	MA		MA
RELAYS																	
419A KTU																	
A	MA4		4	101				L	GRD	1,2			O		17		
								L	GRD	1,2			R		11.9		
B	MB17		4	203				U	L	B/G	2,3		O		21		
								U	L	B/G	2,3		H		7.5		
													NO		11.8		
C	MA4		4	101				U	L	B/G	2,3		O		17		
								U	L	B/G	2,3		R		1.9		
H	MB17		4	203				U	L	B/G	2,3		O		21		
													H		7.5		
													NO		11.8		
R	MA29		4	6				U	L	B/G	2,3		O		4		
RO	MB17		4	203				U	L	B/G	2,3		O		21		
								U	L	B/G	2,3		H		7.5		
													NO		11.8		

TEST NOTES:

1. OPEN A LEAD TO TELEPHONE SETS.
2. RELAY NOT ADJUSTABLE, REPLACE WHERE THERE IS A MALFUNCTION.
3. THE CURRENT FLOW INFORMATION LISTED APPLIES ONLY WHEN THE RELAY TO BE TESTED IS REMOVED FROM THE PRINTED WIRING BOARD.
4. OPEN S LEAD TO TELEPHONE SETS.
5. OPEN TIP AND RING LEADS TO TELEPHONE SET.

PAGE 2

5

TIE LINE AND STATION LINE CIRCUITS

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